## PATENT APPLICATION

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

JC, Terunao HANAOKA

plication No.: 10/654,424

Filed: September 4, 2003

Docket No.: 117031

WIRING BOARD, METHOD OF MANUFACTURING THE SAME, SEMICONDUCTOR

DEVICE, CIRCUIT BOARD, AND ELECTRONIC EQUIPMENT

## INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Pursuant to 37 CFR §1.56, the attention of the Patent and Trademark Office is hereby directed to the reference(s) listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the reference(s) be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

- $\boxtimes$ This Information Disclosure Statement is being filed (a) within three months of 1. the U.S. filing date of this non-CPA application, OR (b) before the mailing date of a first Office Action on the merits in the present application. No certification or fee is required.
- $\boxtimes$ 2. An English-language Abstract of the non-English language reference is attached hereto. (Ref. 3)
- $\boxtimes$ A computer-generated English translation of the following Japanese Patent Publication has been obtained from the website of the Japanese Patent Office ([http://www.jpo.go.jp]), and is attached, but has not been reviewed for accuracy. See Reference 3.

Respectfully submitted,

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JAO:TJP/amo

Date: December 11, 2003

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DEPOSIT ACCOUNT USE AUTHORIZATION Please grant any extension necessary for entry; Charge any fee due to our Deposit Account No. 15-0461

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	2	10/678,215	10/	06/2003	Terunao HANAOKA				
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